

Iterative Error Correction

Turbo, Low-Density Parity-Check and Repeat–Accumulate Codes

Iterative error correction codes have found widespread application in cellular communications, digital broadcasting, deep space communications, and wireless LANs. This self-contained treatment of iterative error correction presents all the key ideas needed to understand, design, implement, and analyze these powerful codes.

Turbo, low-density parity-check, and repeat–accumulate codes are given equal, detailed coverage, with precise presentations of encoding and decoding procedures. Worked examples are integrated into the text to illuminate each new idea and pseudo-code is included for important algorithms to facilitate the reader’s development of the techniques described. For each subject, the treatment begins with the simplest case before generalizing. There is also coverage of advanced topics such as density-evolution and EXIT charts for those readers interested in gaining a deeper understanding of the field. This text is ideal for graduate students in electrical engineering and computer science departments, as well as practitioners in the communications industry.

Sarah J. Johnson is a Research Fellow in the School of Electrical Engineering and Computer Science at the University of Newcastle, Australia. She is a member of the IEEE Information Theory and Communications Societies.

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Sarah J. Johnson
Frontmatter
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SARAH J. JOHNSON

University of Newcastle, New South Wales



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To Ro and Clara

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Preface

The field of error correction coding was launched with Shannon’s revolutionary 1948 work showing – quite counter to intuition – that it is possible to transmit digital data with arbitrarily high reliability over noise-corrupted channels, provided that the rate of transmission is below the capacity of the channel. The mechanism for achieving this reliable communication is to encode a digital message with an error correction code prior to transmission and apply a decoding algorithm at the receiver.

Classical block and convolutional error correction codes were described soon afterwards and the first iterative codes were published by Gallager in his 1962 thesis; however they received little attention until the late 1990s. In the meantime, the highly structured algebraic codes introduced by Hamming, Elias, Reed, Muller, Solomon and Golay among others dominated the field. Despite the enormous practical success of these classical codes, their performance fell well short of the theoretically achievable performances set down by Shannon in his seminal 1948 paper. By the late 1980s, despite decades of attempts, researchers were largely resigned to this seemingly insurmountable theory–practice gap.

The relative quiescence of the coding field was utterly transformed by the introduction of “turbo codes”, proposed by Berrou, Glavieux and Thitimajshima in 1993, wherein all the key ingredients of successful error correction codes were replaced: turbo codes involve very little algebra, employ iterative, distributed, algorithms and focus on average (rather than worst-case) performance. This was a ground-shifting paper, forcing coding theorists to revise strongly held beliefs. Overnight, the gap to the Shannon capacity limit was all but eliminated, using decoders with manageable complexity.

As researchers worked through the 1990s to understand just why turbo codes worked as well as they did, two researchers, McKay and Neal, introduced a new class of block codes designed to possess many features of the new turbo codes. It was soon recognized that these block codes were in fact a rediscovery of the low-density parity-check (LDPC) codes developed years earlier by Gallager. Generalizations of Gallager’s LDPC codes by a number of researchers, including Luby, Mitzenmacher, Shokrollahi, Spielman, Richardson and Urbanke, to irregular LDPC codes actually achieve Shannon’s capacity limits on some

channels and approach Shannon’s capacity to within hundredths of a decibel on others.

A new class of iterative codes introduced by Divsalar, Jin and McEliece, called repeat–accumulate (RA) codes, combine aspects of both turbo and LDPC codes, arguably containing the best features of each. The combination of simplicity with excellent decoding performance has made RA codes a hot topic for the coding community. Indeed, modified RA codes have been shown to be the first capacity-achieving codes with bounded complexity per message bit for both encoding and decoding.

So rapid has progress been in this area that coding theory today is in many ways unrecognizable from its state less than two decades ago. In addition to the strong theoretical interest in iterative codes, such codes have already been adopted in deep space applications, satellite-based digital video broadcasting, long-haul optical communication standards, wireless local area networking and mobile telephony.

This book presents an introductory treatment of these extremely powerful error-correcting codes and their associated decoding algorithms. The focus of the book is almost exclusively on iterative error correction. Indeed, classical coding topics are only covered where they are necessary for the development of iterative codes, as in Chapters 1 and 4. The umbrella term *iterative decoding*, sometimes referred to as *message-passing decoding*, *probabilistic decoding*, decoding of *codes on graphs*, or simply the *turbo principle*, here encompasses the decoding algorithms associated with turbo codes, low-density parity-check codes and repeat–accumulate codes.

The aim of this book is to provide a gentle introduction to turbo, low-density parity-check and repeat–accumulate codes, with equal coverage given to all three. No prior knowledge of coding theory is required. An emphasis on examples is employed to illuminate each new idea and pseudo-code is presented to facilitate the reader’s development of the techniques described. The focus of this book is on ideas and examples rather than theorems and proofs. Where results are mentioned but not proved, references are provided for the reader wishing to delve deeper. Readers interested in a thorough exposition of the theory, however, need go no further than the excellent text by Richardson and Urbanke [1].

None of the material in this text is new; all of it is available in the iterative decoding literature. However, we have decided not to attempt to attribute every idea to the location of its first publication, not least of all because many ideas in the field have been developed independently by a number of authors in different ways. Most importantly, though, we want to keep the focus of the text on explanations of ideas rather than as a historical record. We do use references, however, to point out a few main turning points in the field and to provide direction for the reader wishing to pursue topics we have not fully covered.

Chapters 2 and 3 together can be read as a stand-alone introduction to LDPC codes and Chapters 4 and 5 together provide a stand-alone introduction to turbo codes. Subsequently, however, the distinction is less sharp. The material on RA codes in Chapter 6 draws heavily on the previous material and Chapters 7 and 8 apply to all three code types.

Book outline

Chapter 1 provides a general overview of point-to-point communications systems and the role played by error correction. Firstly we define three channel models, each of which will play an important role in the remainder of the book: the binary symmetric channel (BSC), the binary erasure channel (BEC) and the binary input additive white Gaussian noise channel (BI-AWGNC). From these channel models we introduce the concept of the capacity of a communications channel, a number that precisely captures the minimum redundancy needed in a coded system to permit (essentially) zero errors following decoding. We then turn to error correction and briefly introduce classical error correction techniques and analysis.

Chapters 2 and 3 serve as a self-contained introduction to low-density parity-check codes and their iterative decoding. To begin, block error correction codes are described as a linear combination of parity-check equations and thus defined by their parity-check matrix representation. Iterative decoding algorithms are introduced using a hard decision (bit-flipping) algorithm so that the topic is first developed without reference to probability theory. Subsequently, the sum-product decoding algorithm is presented. Taken alone, Chapter 2 provides a working understanding of the encoding and decoding of LDPC codes without any material on their design and analysis. In Chapter 3 we then discuss the properties of an LDPC code which affect its iterative decoding performance and we present some common construction methods used to produce codes with desired properties.

In Chapters 4 and 5 we turn to turbo codes and their soft, iterative, decoding, which has sparked an intense level of interest in such codes over the past decade. Our starting point is convolutional codes and their trellis representation. This opens the way to the optimal decoding of trellis-based codes using the BCJR algorithm for the computation of maximum a posteriori (MAP) (symbol) probabilities. Having presented the principles of convolutional component codes in Chapter 4, we develop the turbo encoder and decoder in Chapter 5, using the convolutional encoder and MAP decoder as the primary building blocks, and discuss design principles for turbo codes.

In Chapter 6 we consider iterative codes created using serial concatenation. Having so far treated separately the two main classes of iteratively decodable

Rather than consider the behaviour of any particular code, our focus in Chapter 7 shifts to *ensembles* of codes, and the *expected* behaviour of an iterative decoding algorithm over this ensemble. Tracking the evolution of the probability density function through successive iterations of the decoding algorithm, a process known as *density evolution* allows us to compute the thresholds of iterative code ensembles and compare their performance with the channel capacity. The chapter concludes by showing how density evolution can be usefully approximated by extrinsic information transfer (EXIT) analysis techniques to understand better the performance of iterative decoding algorithms.

In Chapter 8 we also use the concept of code ensembles but now focus on low-noise channels and consider the error floor performance of iterative codes. Except for the special case of the binary erasure channel, our analysis considers the properties of the codes independently of their respective iterative decoding algorithms. In fact we assume maximum likelihood decoding, for which the performance of a code depends only on its codeword weight distribution.

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Most of all I owe more than I can say to my family, from whom much of the time to write this book was taken.

Notation

The following is a list of notation used across more than one chapter.

$\alpha_t(S_i)$	Forward metric of the BCJR decoder at time t for state S_i
a	Combiner parameter of an RA code
a_d	Number of codewords of weight d
$a_{w,d}$	Number of messages of weight w that produce codewords of weight d
$\mathbf{A}$	Vector of a priori LLRs
$\mathbf{A}_l^{(j)}$	Vector of a priori LLRs into the j th decoder at the l th iteration of turbo decoding
A_w	Number of messages of weight w
$A_{w,p}$	Number of messages of weight w that produce parity bits of weight p
$A(D)$	Weight enumerating function (of a_d 's)
$A(W, D)$	Input–output weight enumerating function (of $a_{w,d}$'s)
$A(W, P)$	Input–redundancy weight enumerating function (of $A_{w,p}$'s)
$\mathcal{A}(S_i)$	LLR of $\alpha_t(S_i)$
$\beta_t(S_i)$	Backward metric of the BCJR decoder at time t for state S_i
$\mathcal{B}(S_i)$	LLR of $\beta_t(S_i)$
$\mathbf{c}$	Binary vector containing the codeword bits
$c_t^{(i)}$	Value of the codeword bit output at time t from the i th output of a convolutional encoder
$c(x)$	Capacity of a channel with parameter x
C	An error correction code
d	Hamming weight of a binary codeword
$\mathbf{d}$	Binary vector containing the interleaved message bits of an RA code
$d_{\min}$	Minimum distance of an error correction code

d_f	Free distance of a convolutional code
d_{ef}	Effective free distance of a convolutional or concatenated code
$d_f^{(1)}$	Free distance of the inner code in a serially concatenated code system
$d_f^{(0)}$	Free distance of the outer code in a serially concatenated code system
ε	Erasur probability of the binary erasure channel
ϵ	Crossover probability of the binary symmetric channel
$\mathbf{E}$	Vector of extrinsic LLRs
$E_{j,i}$	Extrinsic message from the j th check node to the i th bit node
$\mathbf{E}_l^{(j)}$	Vector of extrinsic LLRs from the j th decoder at the l th iteration of turbo decoding
E_b/N_0	Signal-to-noise ratio of an additive white Gaussian noise channel
$\gamma_t(S_r, S_s)$	State transition metric into the BCJR decoder for states S_r to S_s at time t
$\Gamma_t(S_r, S_s)$	LLR of $\gamma_t(S_r, S_s)$
G	Code generator matrix
$\mathbf{h}$	Row (check node) degree distribution of an irregular LDPC parity-check matrix
h_i	Fraction of Tanner graph check nodes that are degree i
H	Code parity-check matrix
$I(X; Y)$	The mutual information between X and Y
I_x	Identity matrix of size x
I_{max}	Maximum number of decoder iterations
k	Number of input bits to a convolutional encoder at each time instant
k_O	k for the outer code in a serially concatenated code system
k_I	k for the inner code in a serially concatenated code system
k_i	k for the i th code in a parallel concatenated code system
K	Code dimension (length of the messages)
K_O	K for the outer code in a serially concatenated code system
K_I	K for the inner code in a serially concatenated code system
K_i	K for the i th code in a parallel concatenated code system
$\lambda(x)$	The (edge perspective) bit node degree distribution an irregular LDPC code
λ_i	Fraction of Tanner graph edges connected to a degree- i bit node
l	Iteration number
$\mathbf{L}$	Vector containing the LLRs of the decoded bits
$L(x)$	Log likelihood ratio of x
$\log$	Logarithm to the base e

$\log_2$	Logarithm to the base 2
m	Number of parity-check equations in a block code
$m_t(S_r, S_s)$	State transition metric calculated by the BCJR decoder for states S_r to S_s at time t
M_i	Message from the i th bit node
$M_{j,i}$	Message from the i th bit node to the j th check node
n	Number of output bits from a convolutional encoder at each time instant
n_O	n for the outer code in a serially concatenated code system
n_I	n for the inner code in a serially concatenated code system
n_i	n for the i th code in a parallel concatenated code system
N	Code length (length of the codewords)
N_O	N for the outer code in a serially concatenated code system
N_I	N for the inner code in a serially concatenated code system
N_i	N for the i th code in a parallel concatenated code system
$\oplus$	Modulo-2 addition
Π	Permutation sequence
π_i	i th element of permutation sequence
$\mathbf{p}$	Binary vector containing the parity bits
p	Weight of the parity bits in a binary codeword
$p(x)$	Probability of x
P	Matrix giving a puncturing pattern
q	Repetition parameter of an RA code
$Q(x)$	The error function
$\rho(x)$	The (edge perspective) check node degree distribution of an irregular LDPC code
ρ_i	Fraction of Tanner graph edges connected to a degree- i check node
r	Code rate
$\mathbf{R}$	Vector containing the LLRs of the received bits
σ^2	Variance of the additive white Gaussian noise
$s_t^{(i)}$	Value of the i th shift register element at time t in a convolutional encoder
$\mathbf{s}$	Binary vector containing the bits at the output of the RA code combiner
$\mathbf{s}$	Syndrome of a parity-check code
S	Convolutional encoder state
t	Time elapsed
T	Number of trellis segments
$u_t^{(i)}$	Value of the message bit input at time t to the i th input of the convolutional encoder

u	Binary vector containing the message bits
$\hat{\mathbf{u}}$	Binary vector containing the hard decisions on the decoded message bits
v	Memory order of a convolutional encoder
v_i	Fraction of Tanner graph bit nodes which are degree i
v	Binary vector containing the repeated message bits of an RA code
v	Column (bit node) degree distribution of an irregular LDPC parity-check matrix
w	Weight of a binary message
w_c	Column weight (bit node degree) of a regular LDPC parity-check matrix
w_r	Row weight (check node degree) of a regular LDPC parity-check matrix
x	Vector containing the transmitted bits $\in \{+1, -1\}$
y	Vector containing the values received from the channel
z	Vector containing the additive white Gaussian noise

Commonly used abbreviations

APP	a posteriori probability
ARA	accumulate-repeat-accumulate
BCJR	Bahl, Cock, Jelenik and Raviv (algorithm)
BER	bit error rate
BEC	binary erasure channel
DE	density evolution
EXIT	extrinsic information transfer
BI-AWGN	binary input additive white Gaussian noise
BSC	binary symmetric channel
FIR	finite impulse response
FL	finite length
IIR	infinite impulse response
IOWEF	input-output weight enumerating function
IRA	irregular repeat-accumulate
IRWEF	input-redundancy weight enumerating function
LDPC	low-density parity-check
LLR	log-likelihood ratio
MAP	maximum a posteriori probability
ML	maximum likelihood
RA	repeat-accumulate
SC	serially concatenated
SNR	signal-to-noise ratio
WER	word error rate
WEF	weight enumerating function